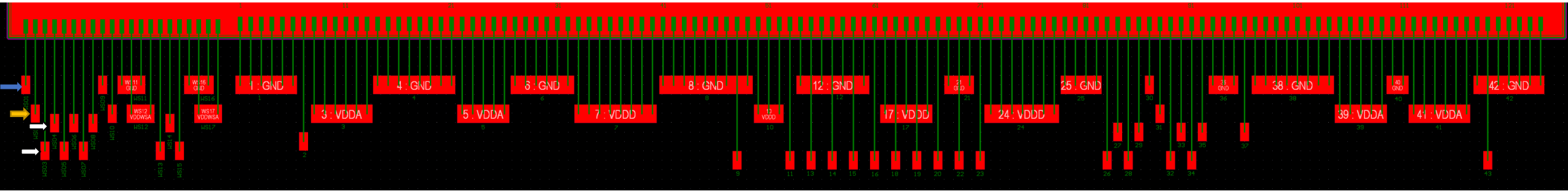


ETROC bonding diagram



- ➡ One row of 16 PCB pads are ground
- ➡ One row of 13 PCB pads are power supplies
- ⇨ Two rows of 31 PCB pads are signals

Smallest PCB pad size : 5x10 mils
Smallest PCB pad pitch : 10.236 mils (0.26 mm)
Smallest bonding wire length : 0.786 mm
Longest bonding wire length : 1.675 mm

| ETROC2 | | | | | |
|----------|----------|-------------|----------|-----------|-------------|
| PCB pin# | Pin name | Die pad# | PCB pin# | Pin name | Die pad# |
| 1 | VSS_A | 1-6 | 23 | DORN | 71 |
| 2 | VREF | 7 | 24 | VDD_D | 72-78 |
| 3 | VDD_A | 8-13 | 25 | VSS_D | 79-82 |
| 4 | VSS_A | 14-21 | 26 | ADDR0 | 83 |
| 5 | VDD_A | 22-26 | 27 | ADDR1 | 84 |
| 6 | VSS_AD | 27-32 | 28 | ADDR2 | 85 |
| 7 | VDD_D | 33-40 | 29 | RSTn | 86 |
| 8 | VSS_D | 41-47,49 | 30 | VSS_D | 87 |
| 9 | VP | 48 | 31 | VDD_D | 88 |
| 10 | VDD_D | 50-52 | 32 | SCL | 89 |
| 11 | CLK40P | 53 | 33 | SDA | 90 |
| 12 | VSS_D | 54,56,58,40 | 34 | ADDR3 | 91 |
| 13 | CLK40N | 55 | 35 | ADDR4 | 92 |
| 14 | CLK1280P | 57 | 36 | VSS_D | 93-95 |
| 15 | CLK1280N | 59 | 37 | VDD_EFUSE | 96 |
| 16 | FCP | 61 | 38 | VSS_D | 97-104 |
| 17 | VDD_D | 62,64,66 | 39 | VDD_A | 105-109 |
| 18 | FCN | 63 | 40 | VSS_D | 110-111 |
| 19 | DOLP | 65 | 41 | VDD_A | 112-117 |
| 20 | DOLN | 67 | 42 | VSS_D | 118,120-124 |
| 21 | VSS_D | 68,70 | 43 | VTEMP | 119 |
| 22 | DORP | 69 | | | |

| Wave Sample | | |
|-------------|----------|-----------|
| PCB pin# | Pin name | Die pad# |
| WS01 | DVSS | WS01 |
| WS02 | DVDD | WS02 |
| WS03 | ADDR4 | WS03 |
| WS04 | ADDR3 | WS04 |
| WS05 | ADDR2 | WS05 |
| WS06 | ADDR1 | WS06 |
| WS07 | ADDR0 | WS07 |
| WS08 | WR_EN | WS08 |
| WS09 | DVSS | WS09 |
| WS10 | DVDD | WS10 |
| WS11 | AVSS | WS11,WS13 |
| WS12 | AVDD | WS12,WS14 |
| WS13 | SCL | WS15 |
| WS14 | SDA | WS16 |
| WS15 | RSTn | WS17 |
| WS16 | AVSS | WS18,WS20 |
| WS17 | AVDD | WS19,WS21 |

PCB-DIE pin map